



STIC Search Report

EIC 2100

STIC Database Tracking Number: 123985

**TO: John Lane
Location: 2Y13
Art Unit : 2188
Tuesday, June 15, 2004**

Case Serial Number: 09/921841

**From: David Holloway
Location: EIC 2100
PK2-4B30
Phone: 308-7794**

david.holloway@uspto.gov

Search Notes

Dear Examiner Lane,

Attached please find your search results for above-referenced case.
Please contact me if you have any questions or would like a re-focused search.

David

Access DB#

123985

SEARCH REQUEST FORM

Scientific and Technical Information Center

(38)

Requester's Full Name: Jack Lane Examiner #: 68699 Date: 06/07/04
 Art Unit: 2188 Phone Number 305-3818 Serial Number: 09/921,841
 Mail Box Location: 2Y13 Results Format Preferred (circle): PAPER DISK E-MAIL

If more than one search is submitted, please prioritize searches in order of need.

Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Include the elected species or structures, keywords, synonyms, acronyms, and registry numbers, and combine with the concept or utility of the invention. Define any terms that may have a special meaning. Give examples or relevant citations, authors, etc, if known. Please attach a copy of the cover sheet, pertinent claims, and abstract.

Title of Invention: Private memory access in multi-node system

Inventors (please provide full names): Carl C. McAdams

Earliest Priority Filing Date: 08/02/01

**For Sequence Searches Only* Please include all pertinent information (parent, child, divisional, or issued patent numbers) along with the appropriate serial number.*

See Abstract, claims and fig 1.

Search:

Base node

Multiple node, multi-node etc.

Coalesced ?

STAFF USE ONLY

Type of Search

Vendors and cost where applicable

Searcher: David H. Howay

NA Sequence (#) _____

STN _____

Searcher Phone #: 308 7791

AA Sequence (#) _____

Dialog 13%

Searcher Location: CPH 2 4830

Structure (#) _____

Questel/Orbit _____

Date Searcher Picked Up: 6-14-04

Bibliographic ✓

Dr.Link _____

Date Completed: 6-15-04

Litigation _____

Lexis/Nexis _____

Searcher Prep & Review Time: 60

Fulltext ✓

Sequence Systems _____

210

Set	Items	Description
S1	42157	NETWORK? OR DISTRIBUTED OR REMOTE()ACCESS? OR MULTINOD? OR (MULTIPL? OR PLURAL? OR SEVERAL OR MANY) ()NODE? OR LAN OR WAN OR LANS OR WANS OR INTRANET?
S2	25500	DESKTOP? OR DESK()TOP? OR NODE? OR CLIENT? ? OR WORKSTATION? OR WORK()STATION?
S3	223	(PRIVATE? OR LOCAL? OR FLASH) () (MEMOR? OR STORAGE?) OR FIRMWARE? OR ERROR() (LOG OR LOGS) OR ERRORLOG? OR EXPANSION() (-ROM OR READ()ONLY) OR NODE()SPECIFIC() (MEMOR? OR STARAGE)
S4	106	(BASE? OR CENTRAL? OR MAIN OR CONTROL OR ADMIN? OR MAIN) () - (NODE? OR STATION?)
S5	71753	CONTROL? OR MANAGE? OR RESTART? OR ACCESS? OR ADMINIST? OR MODIF? OR CHANG?
S6	0	S1 AND S2 AND S3 AND S4 AND S5
S7	51	S1 AND S2 AND S3
S8	0	S7 AND S4
S9	41	S7 AND S5
S10	3	S3(2N)S5 AND S9

File 256:SoftBase:Reviews,Companies&Prods. 82-2004/May
(c)2004 Info.Sources Inc

10/3,K/1

DIALOG(R)File 256:SoftBase:Reviews,Companies&Prods.
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01153281 DOCUMENT TYPE: Product

PRODUCT NAME: Mailbox Provisioner Solution (153281)

Lucent Technologies (586072)
600 Mountain Ave
Murray Hill, NJ 07974 United States
TELEPHONE: (908) 582-8500

RECORD TYPE: Directory

CONTACT: Sales Department

REVISION DATE: 20030617

Lucent Technologies' Mailbox Provisioner Solution streamlines the **management** of mailbox services. A centralized **administration** console allows users to add, delete, and edit mailbox profiles. The product works with billing and other existing **network** systems. It can operate as a standalone, browser-based provisioning tool. Mailbox Provisioner Solution offers subscribers targeted provisioning **management** features. The product provides IT personnel with mailbox renumbering and batch processing options. It also allows users to create mailbox templates, streamlining profile data entry processes. Mailbox Provisioner Solution's **client** interface can be customized for individual users. The system supports graphical, HTTP, and TCP/IP **clients**. Subscribers can define and **manage** outcall schedules and personal distribution lists. The product also includes a voice mail provisioning Web...

...Provisioner Solution can support multiple languages on a single CA/Sierra (TM) system. It includes **access** right, mailbox routing, audit, **error log**, and other **management** features.

DESCRIPTORS: Computer Telephony; **Network** Software; Telephone Messages; Voice Mail; **WANs**

10/3,K/2

DIALOG(R)File 256:SoftBase:Reviews,Companies&Prods.
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00148649 DOCUMENT TYPE: Review

PRODUCT NAMES: WPA (Wi-Fi Protected Access) (803499)

TITLE: Wireless Security: WPA Step by Step
AUTHOR: Ellison, Craig
SOURCE: PC Magazine, v22 n18 p48(4) Fall 2003
ISSN: 0888-8509
HOMEPAGE: <http://www.pcmag.com>

RECORD TYPE: Review

REVIEW TYPE: Product Analysis

GRADE: Product Analysis, No Rating

REVISION DATE: 20040330

PRODUCT NAMES: WPA (Wi-Fi Protected Access) (

To secure a wireless **network**, users should adopt the Wi-Fi Protected Access (WPA) protocol, which is far superior to the Wired Equivalency Protocol (WEP), which has well-known and well-documented security weaknesses. To use WPA, the wireless **access** point, the **client** wireless card, and the **client** wireless software must all support WPA. WPA replaces

WEP entirely, so an upgrade is an...

...nothing proposition. If all of the equipment and software in use supports WPA, upgrade the **client** software first. Microsoft offers a no-charge WPA upgrade for Windows XP from its Microsoft Knowledge Base; other systems may also have WPA-compatible **client** software available for free. Upgrade the **firmware** in the **access** point next. Check with the vendor of the equipment for the appropriate software and instructions to enable WPA in the **access** point. Finally, the drivers and **firmware** for the **client** wireless card should be upgraded. An example upgrade using Microsoft Windows XP and Linksys's WRT54G router and WPC54G **client** card is shown.

DESCRIPTORS: Communications Protocols; Communications Standards; Computer Security; **Network Administration**; **Network** Software; System Monitoring; Wi-Fi; Wireless **Networks**

10/3,K/3

DIALOG(R) File 256:SoftBase:Reviews,Companies&Prods.
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00086567 DOCUMENT TYPE: Review

PRODUCT NAMES: **PhaserLink (595748)**

TITLE: **Web is IS ally in cost-cutting**

AUTHOR: Caton, Michael

SOURCE: PC Week, v13 n1 pN11(1) Jan 8, 1996

ISSN: 0740-1604

RECORD TYPE: Review

REVIEW TYPE: Product Analysis

GRADE: Product Analysis, No Rating

REVISION DATE: 20020630

...550 printer with PhaserLink software that integrates Hypertext Markup Language (HTML) in the device's **firmware**; it allows **administrators** to remotely **manage** printers using software already installed on **network clients**. Such cost-reducing remote **management** can also be used for other **network** hardware. Web-enabled **management** provided by the Tektronix printer uses Netscape Navigator or another Web browser to display printer ...

DESCRIPTORS: HTML; Internet Utilities; **Network Administration**; **Network** Software; Print Utilities; Remote Printing
?ds;show files

Set	Items	Description
S1	8727312	NETWORK? OR DISTRIBUTED OR REMOTE()ACCESS? OR MULTINOD? OR (MULTIPL? OR PLURAL? OR SEVERAL OR MANY) ()NODE? OR LAN OR WAN OR LANS OR WANS OR INTRANET?
S2	4933273	DESKTOP? OR DESK()TOP? OR NODE? OR CLIENT? ? OR WORKSTATION? OR WORK()STATION?
S3	125972	(PRIVATE? OR LOCAL? OR FLASH) () (MEMOR? OR STORAGE?) OR FIRMWARE? OR ERROR() (LOG OR LOGS) OR ERRORLOG? OR EXPANSION() (-ROM OR READ()ONLY) OR NODE()SPECIFIC() (MEMOR? OR STARAGE)
S4	113438	(BASE? OR CENTRAL? OR MAIN OR CONTROL OR ADMIN? OR MAIN) () - (NODE? OR STATION?)
S5	9881	S3(2N) (CONTROL? OR MANAGE? OR RESTART? OR ACCESS? OR ADMINIST? OR MODIF? OR CHANG?)
S6	0	S1(5N)S2(5N)S4(5N)S5
S7	254	S1(10N)S2(10N)S5
S8	2	S4(S)S7
S9	13	S1(S)S5(S)S4
S10	13	S8 OR S9
S11	3	S4(4N)S5
S12	13	S10 OR S11
S13	8	RD (unique items)
S14	6	S13 NOT PY>2001
File 275:Gale Group Computer DB(TM) 1983-2004/Jun 14 (c) 2004 The Gale Group		
File 47:Gale Group Magazine DB(TM) 1959-2004/Jun 11 (c) 2004 The Gale group		
File 636:Gale Group Newsletter DB(TM) 1987-2004/Jun 11 (c) 2004 The Gale Group		
File 16:Gale Group PROMT(R) 1990-2004/Jun 14 (c) 2004 The Gale Group		
File 624:McGraw-Hill Publications 1985-2004/Jun 11 (c) 2004 McGraw-Hill Co. Inc		
File 484:Periodical Abs Plustext 1986-2004/Jun W1 (c) 2004 ProQuest		
File 613:PR Newswire 1999-2004/Jun 14 (c) 2004 PR Newswire Association Inc		
File 813:PR Newswire 1987-1999/Apr 30 (c) 1999 PR Newswire Association Inc		
File 141:Readers Guide 1983-2004/Jun (c) 2004 The HW Wilson Co		
File 621:Gale Group New Prod.Annou.(R) 1985-2004/Jun 14 (c) 2004 The Gale Group		
File 674:Computer News Fulltext 1989-2004/Jun W1 (c) 2004 IDG Communications		
File 369:New Scientist 1994-2004/Jun W1 (c) 2004 Reed Business Information Ltd.		
File 160:Gale Group PROMT(R) 1972-1989 (c) 1999 The Gale Group		
File 635:Business Dateline(R) 1985-2004/Jun 12 (c) 2004 ProQuest Info&Learning		
File 15:ABI/Inform(R) 1971-2004/Jun 14 (c) 2004 ProQuest Info&Learning		
File 9:Business & Industry(R) Jul/1994-2004/Jun 11 (c) 2004 The Gale Group		
File 13:BAMP 2004/May W4 (c) 2004 The Gale Group		
File 610:Business Wire 1999-2004/Jun 14 (c) 2004 Business Wire.		
File 647:CMP Computer Fulltext 1988-2004/May W5 (c) 2004 CMP Media, LLC		
File 148:Gale Group Trade & Industry DB 1976-2004/Jun 14 (c)2004 The Gale Group		

Set	Items	Description
S1	2487878	NETWORK? OR DISTRIBUTED OR REMOTE()ACCESS? OR MULTINOD? OR (MULTIPL? OR PLURAL? OR SEVERAL OR MANY) ()NODE? OR LAN OR WAN OR LANS OR WANS OR INTRANET?
S2	568418	DESKTOP? OR DESK()TOP? OR NODE? OR CLIENT? ? OR WORKSTATION? OR WORK()STATION?
S3	15068	(PRIVATE? OR LOCAL? OR FLASH) () (MEMOR? OR STORAGE?) OR FIRMWARE? OR ERROR() (LOG OR LOGS) OR ERRORLOG? OR EXPANSION() (-ROM OR READ()ONLY) OR NODE()SPECIFIC() (MEMOR? OR STARAGE)
S4	29296	(BASE? OR CENTRAL? OR MAIN OR CONTROL OR ADMIN? OR MAIN) ()-(NODE? OR STATION?)
S5	13639376	CONTROL? OR MANAGE? OR RESTART? OR ACCESS? OR ADMINIST? OR MODIF? OR CHANG?
S6	3	S1 AND S2 AND S3 AND S4 AND S5
S7	730	S1 AND S2 AND S3
S8	7	S7 AND S4
S9	357	S1 AND S3(2N)S5
S10	80	S7 AND S9
S11	0	S10 AND S4
S12	80	S1 AND S2 AND S3(2N)S5
S13	27	S12 AND (CENTRAL? OR REMOTE? OR BASE OR MAIN OR PRIMARY OR ADMIN?)
S14	34	S13 OR S6 OR S8
S15	24	RD (unique items)
S16	21	S15 NOT PY>2001
File	8: Ei Compendex(R)	1970-2004/Jun W1 (c) 2004 Elsevier Eng. Info. Inc.
File	35: Dissertation Abs Online	1861-2004/May (c) 2004 ProQuest Info&Learning
File	202: Info. Sci. & Tech. Abs.	1966-2004/May 14 (c) 2004 EBSCO Publishing
File	65: Inside Conferences	1993-2004/Jun W2 (c) 2004 BLDSC all rts. reserv.
File	2: INSPEC	1969-2004/Jun W1 (c) 2004 Institution of Electrical Engineers
File	94: JICST-EPlus	1985-2004/May W4 (c) 2004 Japan Science and Tech Corp (JST)
File	111: TGG Natl. Newspaper Index (SM)	1979-2004/Jun 14 (c) 2004 The Gale Group
File	233: Internet & Personal Comp. Abs.	1981-2003/Sep (c) 2003 EBSCO Pub.
File	144: Pascal	1973-2004/May W5 (c) 2004 INIST/CNRS
File	34: SciSearch (R) Cited Ref Sci	1990-2004/Jun W1 (c) 2004 Inst for Sci Info
File	99: Wilson Appl. Sci & Tech Abs	1983-2004/May (c) 2004 The HW Wilson Co.

16/5/1 (Item 1 from file: 8)
DIALOG(R)File 8:Ei Compendex(R)
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05932228 E.I. No: EIP01446713787

Title: Architecture of a multi-service line card

Author: Ishizawa, A.; Akiyama, K.; Fujikawa, S.; Tanaka, S.; Komura, H.

Source: NTT R and D v 50 n 7 2001. p 523-529

Publication Year: 2001

CODEN: NTTDEC ISSN: 0915-2326

Language: Japanese

Document Type: JA; (Journal Article) Treatment: T; (Theoretical)

Journal Announcement: 0111W1

Abstract: We have developed a multi-service line card (MULC), which can be accommodated in access systems and interfaces with a customer's terminal. The MULC has programmable hardware, and its dc and ac characteristics can be **changed by firmware**. The characteristics can be changed quickly by a **control** command or **firmware** download from a **remote** operation system (OpS). In particular, the firmware for basic services (POTS, analog pay phone, and basic rate ISDN), which tend to change frequently, is stored in the non-volatile memory of the MULC, so it does not need to be downloaded. The system therefore reduces the time needed to change service characteristics and prevents traffic congestion. For new, non-basic services, firmware must be downloaded from a **remote** OpS to an additional area in the non-volatile memory. Because the MULC has the same input/output interface as existing line cards (e.g., SLIC or BOCU), it can be installed in an existing access system with no need for further modifications. The MULC is intended to replace SLIC or BOCU Cards as line cards in access or switching **node** systems. This paper describes and evaluates the architecture of the MULC.

Descriptors: Broadband **networks**; Interfaces (computer); **Firmware**; Congestion **control** (communication); Switching systems; Telecommunication traffic

Identifiers: Multi-service line cards; Programmable hardware

Classification Codes:

722.2 (Computer Peripheral Equipment)

716 (Electronic Equipment, Radar, Radio & Television); 722 (Computer Hardware)

71 (ELECTRONICS & COMMUNICATION ENGINEERING); 72 (COMPUTERS & DATA PROCESSING)

16/5/6 (Item 6 from file: 8)
DIALOG(R)File 8: Ei Compendex(R)
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01968979 E.I. Monthly No: EI8605037207 E.I. Yearly No: EI86021613
Title: **COMMUNICATION MANAGEMENT SYSTEM GENERATION IN THE DIPS-106
NETWORK OS.**

Author: Kikuchi, Yasuo; Takeda, Toshimitsu; Shigeta, Nobuo
Source: Review of the Electrical Communication Laboratories (Tokyo) v 33
n 4 Jul 1985 p 681-687
Publication Year: 1985
CODEN: RELTAN ISSN: 0418-6338
Language: ENGLISH
Document Type: JA; (Journal Article)
Journal Announcement: 8605

Abstract: This paper describes the system generation mechanism for the new Communication Management Program (CMP-10) making up part of the DIPS-106 **network** operating system. The generation method employed has several distinctive features. **Network** resource information managed by each **network node** has been **centralized** for all **network nodes**. In addition, **network** information used by the communication **control firmware** is integrated with the CMP-10 information, and can thus be managed one-dimensionally. A **network** definition program, in which the **network** information for all **network nodes** is **centralized**, plays a **central** role. (Edited author abstract) 4 refs.

Descriptors: COMPUTER **NETWORKS** ; DIGITAL COMMUNICATION SYSTEMS;
INFORMATION RETRIEVAL SYSTEMS; COMPUTER OPERATING SYSTEMS

Identifiers: COMMUNICATION MANAGEMENT SYSTEM; SYSTEM GENERATION METHOD;
DIPS-106 **NETWORK OS**; **NETWORK** DEFINITION PROGRAM

Classification Codes:

723 (Computer Software); 718 (Telephone & Line Communications)
72 (COMPUTERS & DATA PROCESSING); 71 (ELECTRONICS & COMMUNICATIONS)

16/5/16 (Item 3 from file: 2)
DIALOG(R) File 2:INSPEC
(c) 2004 Institution of Electrical Engineers. All rts. reserv.

5985672 INSPEC Abstract Number: C9809-5440-019

Title: Flexible use of memory for replication/migration in cache-coherent DSM multiprocessors

Author(s): Soundararajan, V.; Heinrich, M.; Verghese, B.; Gharachorloo, K.; Gupta, A.; Hennessy, J.

Author Affiliation: Comput. Syst. Lab., Stanford Univ., CA, USA

Conference Title: Proceedings. 25th Annual International Symposium on Computer Architecture (Cat. No.98CB36235) p.342-55

Publisher: IEEE Comput. Soc, Los Alamitos, CA, USA

Publication Date: 1998 Country of Publication: USA xiii+394 pp.

ISBN: 0 8186 8491 7 Material Identity Number: XX98-01756

U.S. Copyright Clearance Center Code: 1063-6897/98/\$10.00

Conference Title: Proceedings of ISCA 98: International Symposium on Computer Architecture

Conference Sponsor: IEEE Comput. Soc. Tech. Committee on Comput. Archit.; ACM SIGARCH; Comision Interministerial de Ciencia y Tecnol. (CICYT); Comissio Interdept. Recerca i Innovacio Tecnol. (CIRIT); Univ. Politech. Catalunya (UPC)

Conference Date: 27 June-1 July 1998 Conference Location: Barcelona, Spain

Language: English Document Type: Conference Paper (PA)

Treatment: Applications (A); Practical (P)

Abstract: Given the limitations of bus-based multiprocessors, CC-NUMA is the scalable architecture of choice for shared-memory machines. The most important characteristic of the CC-NUMA architecture is that the latency to access data on a **remote node** is considerably larger than the latency to **access local memory**. On such machines, good data locality can reduce memory stall time and is therefore a critical factor in application performance. In this paper we study the various options available to system designers to transparently decrease the fraction of data misses serviced **remotely**. This work is done in the context of the Stanford FLASH multiprocessor. FLASH is unique in that each **node** has a single pool of DRAM that can be used in a variety of ways by the programmable memory controller. We use the programmability of FLASH to explore different options for cache-coherence and data-locality in compute-server workloads. First, we consider two protocols for providing **base** cache-coherence, one with **centralized** directory information (dynamic pointer allocation) and another with **distributed** directory information (SCI). While several commercial systems are based on SCI, we find that a **centralized** scheme has superior performance. Next, we consider different hardware and software techniques that use some or all of the local memory in a **node** to improve data locality. Finally, we propose a hybrid scheme that combines hardware and software techniques. These schemes work on the same **base** platform with both user and kernel references from the workloads. The paper thus offers a realistic and fair comparison of replication/migration techniques that has not previously been feasible. (29 Refs)

Subfile: C

Descriptors: **distributed** memory systems; parallel architectures; performance evaluation; protocols; shared memory systems

Identifiers: replication; migration; cache-coherent DSM multiprocessors; CC-NUMA; scalable architecture; shared-memory machines; data locality; memory stall time; system designers; Stanford FLASH multiprocessor; programmable memory controller; compute-server workloads; protocols; **base** cache-coherence; **centralized** directory information; dynamic pointer allocation; **distributed** directory information; SCI; kernel references

Class Codes: C5440 (Multiprocessing systems); C5220P (Parallel architecture); C5470 (Performance evaluation and testing); C5640 (Protocols)

Copyright 1998, IEE

16/5/20 (Item 1 from file: 233)
DIALOG(R)File 233:Internet & Personal Comp. Abs.
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00243017 91LA06-003

Securecard offers PC users token controlled access Based in firmware , executes before OS loads

Stephenson, Peter

LAN Times , June 1, 1991 , v8 n12 p31, 1 Pages

ISSN: 1040-5917

Company Name: Datamedia

Product Name: Securecard

Languages: English

Document Type: Hardware Review

Grade (of Product Reviewed): B

Geographic Location: United States

Presents a favorable review of Securecard (\$495), an plug-in controller card from Datamedia Corp. of Nashua, NH (603). Says this security system is based in firmware and executes before the operating system loads. It provides a secure **workstation** by installing the Securecard reader in a drive bay and adding the add-in card. It is simple to install and configure. Setup is made easy with provided **administrator** tools. Securecards can be programmed with the reader or with an optional ROM programmer. The card and the PC must match with user name, password, and **workstation** -specific information. Securecards and **workstations** can be programmed to allow or restrict use of the hard drive, floppy disk, printer, or COM ports. Restrictions can be on a user-by-user basis. (v1)

Descriptors: Security; Controller; Expansion Board; **Workstation** ;
Local Area **Networks** ; Hardware Review

Identifiers: Securecard; Datamedia

Set	Items	Description
S1	49	AU=(MCADAMS C? OR MCADAMS, C?)
S2	0	S1 AND (PRIVATE()MEMOR? OR COALESCED OR (BASE OR CONTROL) (-)NODE?)
S3	0	S1 AND (NETWORK? OR NODE? OR MEMOR?)

?show files

File 2:INSPEC 1969-2004/Jun W1
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File 6:NTIS 1964-2004/Jun W2
(c) 2004 NTIS, Intl Cpyrght All Rights Res

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File 148:Gale Group Trade & Industry DB 1976-2004/Jun 14
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File 275:Gale Group Computer DB(TM) 1983-2004/Jun 14
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File 647:CMP Computer Fulltext 1988-2004/May W5
(c) 2004 CMP Media, LLC

File 674:Computer News Fulltext 1989-2004/May W5
(c) 2004 IDG Communications

File 363:Dir. of Chem. Producers-Products 2004/Q1
(c) 2004 SRI International

Set	Items	Description
S1	489073	NETWORK? OR DISTRIBUTED OR REMOTE()ACCESS? OR MULTINOD? OR (MULTIPL? OR PLURAL? OR SEVERAL OR MANY) ()NODE? OR LAN OR WAN OR LANS OR WANS OR INTRANET?
S2	155998	DESKTOP? OR DESK()TOP? OR NODE? OR CLIENT? ? OR WORKSTATION? OR WORK()STATION?
S3	19001	(PRIVATE? OR LOCAL? OR FLASH) () (MEMOR? OR STORAGE?) OR FIRMWARE? OR ERROR() (LOG OR LOGS) OR ERRORLOG? OR EXPANSION() (-ROM OR READ()ONLY) OR NODE()SPECIFIC() (MEMOR? OR STARAGE)
S4	59113	(BASE? OR CENTRAL? OR MAIN OR CONTROL OR ADMIN? OR MAIN) ()-(NODE? OR STATION?)
S5	5498253	CONTROL? OR MANAGE? OR RESTART? OR ACCESS? OR ADMINIST? OR MODIF? OR CHANG?
S6	2	S1 AND S2 AND S3 AND S4 AND S5
S7	12329	S5 AND S3
S8	2	S1 AND S2 AND S3 AND S4
S9	278	S1 AND S2 AND S3
S10	8	S9 AND IC=G06F-015/167
S11	67	S4 AND S7
S12	3	S11 AND IC=G06F-015?
S13	11	S6 OR S8 OR S10 OR S12
S14	1087	S1 AND S7
S15	264	S14 AND IC=G06F-015?
S16	154	S2(5N)S3
S17	23	S15 AND S16
S18	31	S17 OR S13
S19	31	IDPAT (sorted in duplicate/non-duplicate order)
S20	31	IDPAT (primary/non-duplicate records only)

File 347:JAPIO Nov 1976-2004/Feb(Updated 040607)

(c) 2004 JPO & JAPIO

File 350:Derwent WPIX 1963-2004/UD,UM &UP=200437

(c) 2004 Thomson Derwent

20/5/3 (Item 3 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2004 Thomson Derwent. All rts. reserv.

015769569 **Image available**
WPI Acc No: 2003-831771/200377
Related WPI Acc No: 2003-302187
XRPX Acc No: N03-664726

Data access method in distributed shared memory system, involves determining whether latest data are stored in local node based on memory coherency directory, and transmitting latest data to remote node, accordingly

Patent Assignee: WEISHENG ELECTRONICS CO LTD (WEIS-N); VIA TECHNOLOGIES INC (VIAT-N)

Inventor: CHEN W; LAI J; TSENG W; TSEN W

Number of Countries: 004 Number of Patents: 005

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20030191905	A1	20031009	US 2002371194	P	20020409	200377 B
			US 2003405571	A	20030402	
DE 1020310340	A1	20031023	DE 12003010340	A	20030310	200377
CN 1447255	A	20031008	CN 2003109389	A	20030408	200403
GB 2389207	A	20031203	GB 20034915	A	20030304	200404
GB 2389207	B	20040512	GB 20034915	A	20030304	200432

Priority Applications (No Type Date): TW 2002113128 A 20020617

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20030191905	A1		11	G06F-012/00	Provisional application US 2002371194

DE 1020310340	A1	G06F-013/36
CN 1447255	A	G06F-015/16
GB 2389207	A	G06F-012/08
GB 2389207	B	G06F-012/08

Abstract (Basic): US 20030191905 A1

NOVELTY - The storage of latest data in a **local memory** of a **local node**, is determined according to a memory coherency directory which defines HOME-M, HOME-N, SHARED, GONE and WASH status of the **local memory**, in response to a request from a remote node. The latest data are received through an internal bus and transmitted to the remote node, based on the determination result.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for **distributed shared memory system**.

USE - For **accessing** data from a **local memory** by a remote **node**, in **distributed shared memory (DSM) system** (claimed).

ADVANTAGE - Enables preventing unnecessary confirmation of position of data in the **local memory** line by asserting a system bus transaction, thereby alleviating the burden of the system bus and improving its performance. The undesired delay of the remote **node** for reading data from the **local memory** is prevented.

DESCRIPTION OF DRAWING(S) - The figure shows a flowchart illustrating data **access** process.

pp; 11 DwgNo 4/4

Title Terms: DATA; **ACCESS**; METHOD; DISTRIBUTE; SHARE; MEMORY; SYSTEM; DETERMINE; LATE; DATA; STORAGE; LOCAL; NODE; BASED; MEMORY; COHERE; DIRECTORY; TRANSMIT; LATE; DATA; REMOTE; NODE; ACCORD

Derwent Class: T01

International Patent Class (Main): G06F-012/00; G06F-012/08; G06F-013/36; **G06F-015/16**

International Patent Class (Additional): G06F-013/14

File Segment: EPI

20/5/6 (Item 6 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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015292902 **Image available**

WPI Acc No: 2003-353836/200333

XRFX Acc No: N03-282719

Private memory access method for multinode computer network ,
involves forwarding instructions to nodes from main node to
initiate memory process and reporting results to main node of
coalesced system

Patent Assignee: INT BUSINESS MACHINES CORP (IBM)

Inventor: MCADAMS C C

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20030037118	A1	20030220	US 2001921841	A	20010802	200333 B

Priority Applications (No Type Date): US 2001921841 A 20010802

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

US 20030037118 A1 12 G06F-015/167

Abstract (Basic): US 20030037118 A1

NOVELTY - A process related to private memory is initiated by forwarding instructions from main node to other nodes of a coalesced system. The private memory is accessed based on the instructions received by the node and the processing results are reported back to the main node from each node of the coalesced system.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:

(1) private memory access system; and

(2) article comprising computer readable medium for private memory access

USE - For managing private memory access in multi node computer network .

ADVANTAGE - Enlarges performance of memory system by managing the node performance individually and rebooting of nodes . Simplifies performance management by eliminating the need for personal process initiation at nodes .

DESCRIPTION OF DRAWING(S) - The figure shows a flowchart representing printing process.

pp; 12 DwgNo 1/7

Title Terms: PRIVATE; MEMORY; ACCESS ; METHOD; COMPUTER; NETWORK ;

FORWARDING; INSTRUCTION; NODE ; MAIN; NODE ; INITIATE; MEMORY; PROCESS;

REPORT; RESULT; MAIN; NODE ; COALESCE; SYSTEM

Derwent Class: T01; U14

International Patent Class (Main): G06F-015/167

File Segment: EPI

20/5/8 (Item 8 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2004 Thomson Derwent. All rts. reserv.

014843886 **Image available**
WPI Acc No: 2002-664592/200271
XRPX Acc No: N02-525648

Node controller for broadcasting invalidation messages in memory system of computers, has cross bar unit to transfer data and invalidation messages between memory directory unit, network interface unit and local buffer

Patent Assignee: SILICON GRAPHICS INC (SILI-N)
Inventor: KOREN Y
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6421712	B1	20020716	US 99356106	A	19990716	200271 B

Priority Applications (No Type Date): US 99356106 A 19990716

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 6421712	B1	8	G06F-015/167	

Abstract (Basic): US 6421712 B1

NOVELTY - A local buffer (28) generates several invalidation messages in response to request from local processor. A cross bar unit (30) transfers data and invalidation messages between memory directory unit (22), **network** interface unit (20) and local buffer (28). The memory directory unit controls access of data within **local memory** device. The **network** interface unit (20) provides data and control messages to other **node** controllers.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is included for invalidation messages broadcasting method.

USE - For broadcasting invalidation messages in memory system of computers.

ADVANTAGE - Prevents blocking of **node** controller operation during processing of invalidation requests. Enables efficient processing of all classes of traffic despite having a large number of invalidation request to process. Enables local processing invalidation requests even when the altered memory location is associated with a remote **node** controller.

DESCRIPTION OF DRAWING(S) - The figure shows a block diagram of illustrating the processing and broadcasting of invalidation messages in the **node** controller.

Network interface unit (20)
Memory directory unit (22)
Local buffer (28)
Cross bar unit (30)
pp; 8 DwgNo 4B/4

Title Terms: **NODE** ; CONTROL; BROADCAST; INVALID; MESSAGE; MEMORY; SYSTEM; COMPUTER; CROSS; BAR; UNIT; TRANSFER; DATA; INVALID; MESSAGE; MEMORY; DIRECTORY; UNIT; **NETWORK** ; INTERFACE; UNIT; LOCAL; BUFFER

Derwent Class: T01

International Patent Class (Main): G06F-015/167

File Segment: EPI

20/5/10 (Item 10 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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014223096 **Image available**
WPI Acc No: 2002-043794/200206
XRPX Acc No: N02-032455

Firmware **image updating procedure for network apparatus, involves processing of firmware image received from client depending on client's connection demand and accordingly update new firmware image**
Patent Assignee: ZHIBANG SCI TECH CO LTD (ZHIB-N); ACCTON TECHNOLOGY CORP (ACCT-N)

Inventor: CHUNG S

Number of Countries: 002 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 2001101018	A	20010413	JP 2000220870	A	20000721	200206 B
TW 448370	A	20010801	TW 99116889	A	19990929	200222

Priority Applications (No Type Date): TW 99116889 A 19990929

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
JP 2001101018	A	8	G06F-011/00	
TW 448370	A		G06F-015/163	

Abstract (Basic): JP 2001101018 A

NOVELTY - A **firmware** image indicating name of a file registered in hypertext transfer protocol program is input by client's (12) web browser to **network** apparatus (11). The **firmware** image received is processed depending on connection demand of client by primary memory device confirming a new **firmware** image. The new **firmware** image is updated and loaded to secondary memory device and the **network** apparatus re-started.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for **firmware** image updating system of a **network** apparatus.

USE - For updating **firmware** image of **network** apparatus such as switch, hub or a router e.g. in download environments such as XMODEM or YMODEM and in serial download environments such as VT-100 terminal.

ADVANTAGE - The client is connected with the hypertext transfer protocol server by simple and stable **firmware** image updating procedure and unnecessary disconnection of client with server is eliminated. The client is able to **access** the server from remote places.

DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of **firmware** image updating system of **network** apparatus. (Drawing includes non-English language text).

Network apparatus (11)

Client (12)

pp; 8 DwgNo 1/4

Title Terms: **FIRMWARE** ; IMAGE; UPDATE; PROCEDURE; **NETWORK** ; APPARATUS; PROCESS; **FIRMWARE** ; IMAGE; RECEIVE; CLIENT; DEPEND; CLIENT; CONNECT; DEMAND; ACCORD; UPDATE; NEW; **FIRMWARE** ; IMAGE

Derwent Class: T01

International Patent Class (Main): G06F-011/00; **G06F-015/163**

International Patent Class (Additional): G06F-001/00; G06F-009/445;

H04L-012/28; H04L-012/54; H04L-012/58

File Segment: EPI

Set	Items	Description
S1	352976	NETWORK? OR DISTRIBUTED OR REMOTE()ACCESS? OR MULTINOD? OR (MULTIPL? OR PLURAL? OR SEVERAL OR MANY) ()NODE? OR LAN OR WAN OR LANS OR WANS OR INTRANET?
S2	136170	DESKTOP? OR DESK()TOP? OR NODE? OR CLIENT? ? OR WORKSTATIO- N? OR WORK()STATION?
S3	31543	(PRIVATE? OR LOCAL? OR FLASH) () (MEMOR? OR STORAGE?) OR FI- RMWARE? OR ERROR() (LOG OR LOGS) OR ERRORLOG? OR EXPANSION() (- ROM OR READ()ONLY) OR NODE()SPECIFIC() (MEMOR? OR STARAGE)
S4	36450	(BASE? OR CENTRAL? OR MAIN OR CONTROL OR ADMIN? OR MAIN) ()- (NODE? OR STATION?)
S5	1826363	CONTROL? OR MANAGE? OR RESTART? OR ACCESS? OR ADMINIST? OR MODIF? OR CHANG?
S6	496	S1(10N)S2(10N)S3(10N)S5
S7	26	S1(10N)S2(10N)S3(10N)S4
S8	2	S6 AND IC=G06F-015/167
S9	2	S7 AND IC=G06F?
S10	4	S8 OR S9
S11	4	IDPAT (sorted in duplicate/non-duplicate order)
S12	826	S5(3N)S3(10N)S1
S13	16	S12(S)S4
S14	87	S12(10N)S2
S15	12	S14 AND IC=G06F-015?
S16	3	S13 AND IC=(G06F? OR H04L?)
S17	19	S16 OR S15 OR S11
S18	19	IDPAT (sorted in duplicate/non-duplicate order)
S19	18	IDPAT (primary/non-duplicate records only)

File 348:EUROPEAN PATENTS 1978-2004/Jun W02

(c) 2004 European Patent Office

File 349:PCT FULLTEXT 1979-2002/UB=20040610,UT=20040603

(c) 2004 WIPO/Univentio

Set	Items	Description
S1	352976	NETWORK? OR DISTRIBUTED OR REMOTE()ACCESS? OR MULTINOD? OR (MULTIPL? OR PLURAL? OR SEVERAL OR MANY) ()NODE? OR LAN OR WAN OR LANS OR WANS OR INTRANET?
S2	136170	DESKTOP? OR DESK()TOP? OR NODE? OR CLIENT? ? OR WORKSTATION? OR WORK()STATION?
S3	31543	(PRIVATE? OR LOCAL? OR FLASH) () (MEMOR? OR STORAGE?) OR FIRMWARE? OR ERROR() (LOG OR LOGS) OR ERRORLOG? OR EXPANSION() (-ROM OR READ()ONLY) OR NODE()SPECIFIC() (MEMOR? OR STARAGE)
S4	36450	(BASE? OR CENTRAL? OR MAIN OR CONTROL OR ADMIN? OR MAIN) ()-(NODE? OR STATION?)
S5	1826363	CONTROL? OR MANAGE? OR RESTART? OR ACCESS? OR ADMINIST? OR MODIF? OR CHANG?
S6	496	S1(10N)S2(10N)S3(10N)S5
S7	26	S1(10N)S2(10N)S3(10N)S4
S8	2	S6 AND IC=G06F-015/167
S9	2	S7 AND IC=G06F?
S10	4	S8 OR S9
S11	4	IDPAT (sorted in duplicate/non-duplicate order)
S12	826	S5(3N)S3(10N)S1
S13	16	S12(S)S4
S14	87	S12(10N)S2
S15	12	S14 AND IC=G06F-015?
S16	3	S13 AND IC=(G06F? OR H04L?)
S17	19	S16 OR S15 OR S11
S18	19	IDPAT (sorted in duplicate/non-duplicate order)
S19	18	IDPAT (primary/non-duplicate records only)

File 348:EUROPEAN PATENTS 1978-2004/Jun W02

(c) 2004 European Patent Office

File 349:PCT FULLTEXT 1979-2002/UB=20040610,UT=20040603

(c) 2004 WIPO/Univentio

19/3,K/3 (Item 3 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00907308

Parallel programming of a plurality of nodes in a communication network
Parallelprogrammierung von mehreren Knoten in einem Kommunikationsnetz
Programmation parallele d'une pluralite de noeuds dans un reseau de
communication

PATENT ASSIGNEE:

New York Air Brake Corporation, (2225430), 748 Starbuck Avenue,
Watertown, New York 13601, (US), (Proprietor designated states: all)

INVENTOR:

Smyrl, Clifford G., 781 W. Hoyt Avenue, St. Paul, Minnesota 55117, (US)
Knight, Douglas G., 504 W. Franklin Avenue, No. 3B, Minneapolis,
Minnesota 55405, (US)

LEGAL REPRESENTATIVE:

Specht, Peter, Dipl.-Phys. et al (78571), Loesenbeck, Stracke,
Loesenbeck, Patentanwalte, Jollenbecker Strasse 164, 33613 Bielefeld,
(DE)

PATENT (CC, No, Kind, Date): EP 828213 A2 980311 (Basic)
EP 828213 A3 000802
EP 828213 B1 030521

APPLICATION (CC, No, Date): EP 97114955 970829;

PRIORITY (CC, No, Date): US 713842 960913

DESIGNATED STATES: DE; FR; GB; IT; SE

INTERNATIONAL PATENT CLASS: G06F-009/445 ; B61L-015/00

ABSTRACT WORD COUNT: 140

NOTE:

Figure number on first page: NONE

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	199811	773
CLAIMS B	(English)	200321	825
CLAIMS B	(German)	200321	779
CLAIMS B	(French)	200321	904
SPEC A	(English)	199811	2187
SPEC B	(English)	200321	2256
Total word count - document A			2961
Total word count - document B			4764
Total word count - documents A + B			7725

INTERNATIONAL PATENT CLASS: G06F-009/445 ...

...SPECIFICATION program can be used and if not, two separate executorial programs must be used.

The **network** in the preferred embodiment is a LonWorks **network** where each of the **nodes** are Neuron **based nodes**. With the presently available hardware, **flash memory** is provided to each target **node** to allow reprogramming. The programming of the target **nodes** in parallel is performed asynchronously. Preferably, the controller is on one of the locomotives of...A and subsequently receive a second file including a second executable program for programming target **node B**.

It should be noted with the present hardware available for LonWorks, the target **nodes** 14 must include or have added a **flash memory** or equivalent device to allow **network based node** reprogramming.

The source **node** 12 prepares the executable program in file form to be transferred to the intermediate **nodes** 18. As described in Echelon Engineering Bulletin, 005-0025-01 Rev. A, dated April, 1993...

19/3,K/4 (Item 4 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00857278

VIRTUAL MAINTENANCE NETWORK IN MULTIPROCESSING SYSTEM
VIRTUELLES UBERWACHUNGSNETZWERK IN EINEM MEHRPROZESSORSYSTEM
RESEAU VIRTUEL DE MAINTENANCE DANS UN SYSTEME MULTIPROCESSEUR
PATENT ASSIGNEE:

CRAY RESEARCH, INC., (578485), 655A Lone Oak Drive, Eagan, Minnesota
55121, (US), (Proprietor designated states: all)

INVENTOR:

THORSON, Gregory, M., 1119 Sweet Water Close, Altoona, WI 54720, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. High Holborn
2-5 Warwick Court, London WC1R 5DJ, (GB)

PATENT (CC, No, Kind, Date): EP 858633 A1 980819 (Basic)
EP 858633 B1 991124
WO 9716792 970509

APPLICATION (CC, No, Date): EP 96932293 960923; WO 96US15117 960923

PRIORITY (CC, No, Date): US 550992 951031

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-015/163

NOTE:

No A-document published by EPO

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9947	962
CLAIMS B	(German)	9947	961
CLAIMS B	(French)	9947	1214
SPEC B	(English)	9947	6154
Total word count - document A			0
Total word count - document B			9291
Total word count - documents A + B			9291

INTERNATIONAL PATENT CLASS: G06F-015/163

...SPECIFICATION system 20 does not include optional BLT 32, and instead
relies on system software to **control** movement of blocks of data between
the **local memories** of the processing elements.

Network interface 34 formats information to be sent over interconnect
network 24 to another processing element **node** or I/O gateway 28.

Network interface 34 also receives incoming information from other
processing...

Set	Items	Description
S1	10	AU=(MCADAMS C? OR MCADAMS, C?)
S2	1	S1 AND IC=G06F-015?
S3	10	IDPAT S1 (sorted in duplicate/non-duplicate order)
S4	7	IDPAT S1 (primary/non-duplicate records only)

File 347:JAPIO Nov 1976-2004/Feb(Updated 040607)
(c) 2004 JPO & JAPIO

File 348:EUROPEAN PATENTS 1978-2004/Jun W02
(c) 2004 European Patent Office

File 349:PCT FULLTEXT 1979-2002/UB=20040610,UT=20040603
(c) 2004 WIPO/Univentio

4/5/1 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2004 Thomson Derwent. All rts. reserv.

015292902 **Image available**
WPI Acc No: 2003-353836/200333
XRPX Acc No: N03-282719

Private memory access method for multinode computer network, involves forwarding instructions to nodes from main node to initiate memory process and reporting results to main node of coalesced system

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)

Inventor: MCADAMS C C

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20030037118	A1	20030220	US 2001921841	A	20010802	200333 B

Priority Applications (No Type Date): US 2001921841 A 20010802

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 20030037118	A1	12	G06F-015/167	

Abstract (Basic): US 20030037118 A1

NOVELTY - A process related to private memory is initiated by forwarding instructions from main node to other nodes of a coalesced system. The private memory is accessed based on the instructions received by the node and the processing results are reported back to the main node from each node of the coalesced system.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:

- (1) private memory access system; and
- (2) article comprising computer readable medium for private memory access

USE - For managing private memory access in multi node computer network.

ADVANTAGE - Enlarges performance of memory system by managing the node performance individually and rebooting of nodes. Simplifies performance management by eliminating the need for personal process initiation at nodes.

DESCRIPTION OF DRAWING(S) - The figure shows a flowchart representing printing process.

pp; 12 DwgNo 1/7

Title Terms: PRIVATE; MEMORY; ACCESS; METHOD; COMPUTER; NETWORK; FORWARDING
; INSTRUCTION; NODE; MAIN; NODE; INITIATE; MEMORY; PROCESS; REPORT;
RESULT; MAIN; NODE; COALESCE; SYSTEM

Derwent Class: T01; U14

International Patent Class (Main): G06F-015/167

File Segment: EPI